

PETITION I	FOR EXTENSION OF TIME UNDER 37 CFR 1.136(a)	Docket Number: SC11391TC
in re Application of Sanchez et al.		
Application Number	09/877,792 Filed 8 June 2001	
For	METHOD AND APPARATUS FOR DESIGNING AND MAKING AN INTEGRATED CIRCUIT	
Group Art Unit 2123 Examiner Proctor, Jason Scott		
This is a request under the provisions of 37 CFR 1.136(a) to extend the period for filing a reply in the above-identified application. The requested extension and appropriate non-small-entity fee are as follows: (Check time period desired): X		
Payment by credit card. Form PTO-2038 is attached. X The Commissioner has already been authorized to charge fees in this application to a Deposit Account. X The Commissioner is hereby authorized to charge any fees which may be required or credit any overpayment to Deposit Account Number 503079, Freescale Semiconductor, Inc. X I have enclosed a duplicate copy of this sheet. I am the: Applicant/inventor Assignee of record of the entire interest. See 37 CFR 3.71.		
Attorney or agent of record (Registration No.: 44,383) Attorney or agent under 37 CFR 1.34(a) Registration number if acting under 37 CFR 1.34(a) Date Signature Joseph T. Van Leeuwen Type or printed name NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.		
X Total of 1 form(s)are submitted		
CERTIFICATE OF MAILING		
I hereby certify that this correspondence is being facsimile transmitted or deposited with the United States Postal Service with sufficient postage as		
first-class mail in an envelope	addressed to: Commissioner for Patents, Washington, DC 20231 on this date:	216.05
Typed or printed name Pat Thomas		
Signature Last Llamas		

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